

LETTERS

Strong quantum-confined Stark effect in germanium quantum-well structures on silicon

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Silicon is the dominant semiconductor for electronics, but there is now a growing need to integrate such components with optoelectronics for telecommunications and computer interconnections¹. Silicon-based optical modulators have recently been successfully demonstrated^{2,3}; but because the light modulation mechanisms in silicon⁴ are relatively weak, long (for example, several millimetres) devices² or sophisticated high-quality-factor resonators³ have been necessary. Thin quantum-well structures made from III-V semiconductors such as GaAs, InP and their alloys exhibit the much stronger quantum-confined Stark effect (QCSE) mechanism⁵, which allows modulator structures with only micrometres of optical path length^{6,7}. Such III-V materials are unfortunately difficult to integrate with silicon electronic devices. Germanium is routinely integrated with silicon in electronics⁸, but previous silicon–germanium structures have also not shown strong modulation effects^{9–13}. Here we report the discovery of the QCSE, at room temperature, in thin germanium quantum-well structures grown on silicon. The QCSE here has strengths comparable to that in III-V materials. Its clarity and strength are particularly surprising because germanium is an indirect gap semiconductor; such semiconductors often display much weaker optical effects than direct gap materials (such as the III-V materials typically used for optoelectronics). This discovery is very promising for small, high-speed¹⁴, low-power^{15–17} optical output devices fully compatible with silicon electronics manufacture.

Quantum wells are thin (for example, 10 nm) layers of semiconductors surrounded by barrier materials. Usually the barriers are chosen to confine electrons in the conduction band and holes (or absences of electrons) in the valence band inside the quantum well; this is called type-I band alignment. The QCSE gives strong spectral shifts of the optical absorption edge with applied electric field near the direct bandgap (that is, a band structure in which the energy minima for electrons and holes lie at the same momentum) in such type-I quantum wells¹⁸. Usually these relevant band minima are at zero momentum (the Γ point, or ‘zone centre’).

The QCSE is routinely used in high-performance quantum-well modulators for telecommunications. It possesses a number of attractive properties that allow for modulators with only micrometres of path length that can be incorporated into large arrays (for example, >38,000 devices) attached to complementary metal-oxide-semiconductor (CMOS) silicon circuits⁶, as well as devices at telecommunications wavelengths of $\sim 1.5\ \mu\text{m}$ (ref. 7). Waveguide QCSE devices typically have lengths of only $\sim 100\text{--}400\ \mu\text{m}$ (see, for example, ref. 14). Recent work¹⁹ has demonstrated devices in InGaAs/InP quantum wells without waveguides that exhibit useful modulation at telecommunications wavelengths for $<1\ \text{V}$ drive, and with the relaxed alignment required for practical packaging. QCSE

devices do not require carrier injection, and are typically operated as reverse-biased diodes. The resulting low power dissipation—of the order of 10 mW per channel—allows large arrays of optical interconnects operating at high data rates from silicon chips^{15–17}. Theoretically, the QCSE is thought to operate at subpicosecond times^{20,21}, and devices with $>50\ \text{GHz}$ modulation bandwidth have been demonstrated¹⁴.

Unlike the III-V compounds typically used for QCSE modulators, both silicon and germanium have indirect lowest-energy bandgaps (meaning that the electron and hole energy minima have different momenta). Previously, type-I SiGe/Si quantum wells have shown no or inefficient QCSE^{9–11}, whereas SiGe/Si quantum wells¹² and Ge/Si quantum dots¹³ (both of which have type-II band alignment in which the electrons and holes have minimum energy in different material layers) can exhibit large shifts of optical transitions with electric field, but have low absorption efficiency. Here we demonstrate clear quantum confinement effects in the optical absorption spectra of Ge quantum wells, associated with zone centre transitions, and in addition a clear and strong QCSE electroabsorption. Though germanium’s lowest bandgap is indirect, we exploit its direct bandgap at $\sim 0.8\ \text{eV}$ at room temperature; the band structure associated with this direct bandgap is qualitatively identical to that in III-V QCSE materials. There is also indirect absorption present at the same and

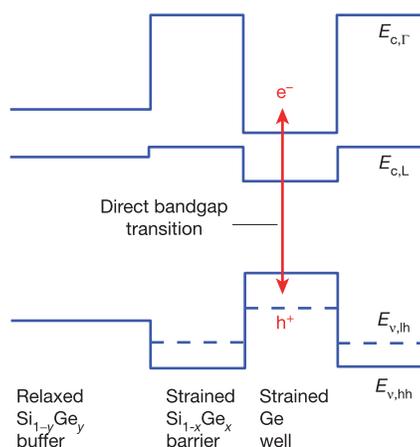


Figure 1 | The bandgap structure of a Ge/SiGe quantum well (not to scale). $E_{c,\Gamma}$ and $E_{c,L}$ are the energies of the bottom of the conduction band at zone centre (Γ point) and at the L valleys, respectively. $E_{v,lh}$ and $E_{v,h}$ are the energies of the tops of the light-hole and heavy-hole valence bands, respectively. The Ge/Si_{0.15}Ge_{0.85} quantum well on relaxed Si_{0.1}Ge_{0.9} has type-I alignment at the zone centre and quantum-confines carriers inside the Ge well.

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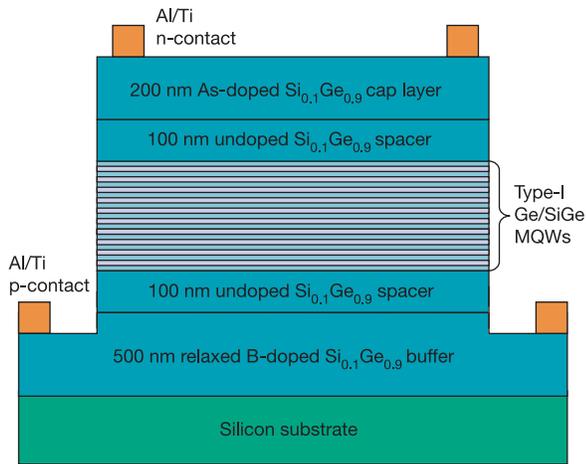


Figure 2 | Schematic diagram of a p-i-n diode. The cross-sectional view shows the structure of strained Ge/SiGe multiple quantum wells (MQWs) grown on silicon on relaxed SiGe direct buffers (not to scale). In the measurements, light from a monochromator is incident on the top surface (that is, in a 'surface-normal' configuration) on the open area inside the rectangular frame top electrode (that is, between the portions of the AlTi n-contacts shown in this cross-section).

lower photon energies, but it is much weaker, allowing the direct optical absorption to dominate.

We use strain-balanced Ge/SiGe multiple quantum wells (MQWs) grown on a relaxed Ge-rich SiGe buffer on silicon, giving type-I alignment at the Γ point. In strain-balanced structures, the average silicon concentration in the Ge/SiGe MQW layers equals that of the buffer layer, allowing the growth of thick structures. Figure 1 shows the resulting bandgap alignment. The band discontinuities of the heavy hole, light hole, and electron at the Γ point between our wells and barriers are calculated to be 101 meV, 47 meV, and 400 meV respectively, based on refs 22–24 with linear interpolation of the direct bandgap of SiGe between Si (to the Γ -2' band) and Ge. Note that this 400 meV Γ -point band discontinuity provides strong electron quantum confinement in the conduction band. There are, of course, lower-energy conduction band minima (L valleys) in the SiGe barriers. For quantum confinement, however, we expect the relevant bands in the barriers to be those with similar unit cell symmetries, as we expect that it is only to such bands that strong tunnelling can exist. Hence, we consider here the bands at the Γ point in the barriers for the calculation of quantum confinement effects. Because the Γ point in Ge wells is higher than the L valleys in the SiGe barriers, we do however expect that, even though the Γ point electron quantum well is relatively deep, electrons generated in the Ge wells will be rapidly scattered out into those L valleys where they can be swept out by electric fields (though not so fast as to prevent the quantum confinement exploited in optical absorption).

Figure 2 shows the Ge/SiGe MQW p-i-n diode structure. The layers are grown sequentially in a commercially available, single-wafer, cold-wall, reduced-pressure, chemical vapour deposition (RPCVD) reactor, using hydrogen carrier gas and silane and germane precursor gases. We use four-inch, boron-doped, (001)-oriented silicon wafer substrates with resistivity 10–20 Ω cm. After an *in situ* high-temperature cleaning, the layers are grown at 400 $^{\circ}$ C, with annealing at higher temperatures. Two 250-nm $\text{Si}_{0.1}\text{Ge}_{0.9}$ films doped with $5 \times 10^{18} \text{ cm}^{-3}$ boron atoms are grown sequentially and annealed at 850 $^{\circ}$ C for 30 min and 700 $^{\circ}$ C for 5 min, respectively, to reduce dislocations caused by the lattice mismatch and to form relaxed p-type buffer layers. A 100 nm undoped $\text{Si}_{0.1}\text{Ge}_{0.9}$ spacer layer is grown, followed by ten pairs of MQWs (10 nm Ge well/16 nm $\text{Si}_{0.15}\text{Ge}_{0.85}$ barrier) and another 100 nm undoped $\text{Si}_{0.1}\text{Ge}_{0.9}$ spacer layer. Finally, the structure is capped by an arsenic-doped $\text{Si}_{0.1}\text{Ge}_{0.9}$

layer with a doping level of 10^{19} cm^{-3} . Each wafer is then patterned by standard lithography and dry-etched to form square mesa structures with widths ranging from 200 to 1,400 μm . Al/Ti metal is evaporated, lifted off, and rapid-thermal-annealed to form rectangular frame ohmic contacts. Because the top As-doped (n-type) SiGe layer is expected to be strongly conducting, the electric field should be essentially perpendicular to the quantum wells throughout the structure. All materials, processing equipment and temperatures used are compatible with the processes used in fabricating CMOS silicon electronics.

We measure photocurrent spectra at room temperature for different diode reverse bias voltages using a chopped quartz-tungsten-halogen light bulb source with a 0.25 m monochromator with a 600 lines mm^{-1} grating and a 400 μm slit, and a lock-in amplifier. The light is incident normal to the surface, with random polarization in the surface plane. As in other quantum-well structures, we expect light- and heavy-hole transitions both to be allowed in this configuration. The responsivity (current per unit optical power) inside the device is deduced from the light intensity incident upon the open area of the mesa surface, correcting for surface reflection. The corresponding effective absorption coefficient spectra (Fig. 3) are calculated assuming one electron of current for every absorbed photon, as is common in fully depleted p-i-n diodes. (Presuming that there is no avalanche gain, this assumption at worst underestimates the absorption. The independence of the photocurrent on bias at high photon energies (for example, 0.94 eV) suggests no bias dependence of the number of electrons per photon, and hence no avalanche gain.) Clear quantum confinement is seen, with strong exciton peaks that we assign to electron-to-heavy-hole (e-hh; $\sim 0.88 \text{ eV}$ at 0 V) and electron-to-light-hole (e-lh; $\sim 0.91 \text{ eV}$ at 0 V) transitions. The effective absorption coefficient at the e-hh exciton peak, calculated on the basis of the total thickness of the wells and the barriers ($\sim 0.26 \mu\text{m}$), is $6,320 \text{ cm}^{-1}$. The half-width at half-maximum of the heavy-hole exciton peak is only $\sim 8 \text{ meV}$, and the heavy-hole exciton peak is still clearly resolved even at $8 \times 10^4 \text{ V cm}^{-1}$ (3 V bias), indicating that the electric field is uniform inside the structure, which in turn implies very low background doping in the intrinsic region. The absorption edge at zero bias is $\sim 80 \text{ meV}$ higher than that of bulk unstrained Ge; this shift is near the calculated sum from quantum-well confinement (56 meV) and strain-induced shifts (36 meV)²³. As the quantum confinement energy originates primarily from the electron, the clarity of this quantum shift shows that the (quantum-mechanical) confinement at the Γ point is strong,

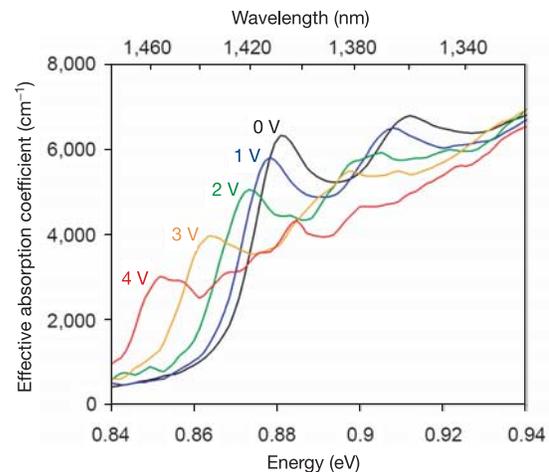


Figure 3 | Effective absorption coefficient spectra. Strong QCSE is observed at room temperature with reverse bias from zero to 4 V. The thickness for effective absorption coefficient calculations is based on the combination of Ge well and SiGe barrier thicknesses.

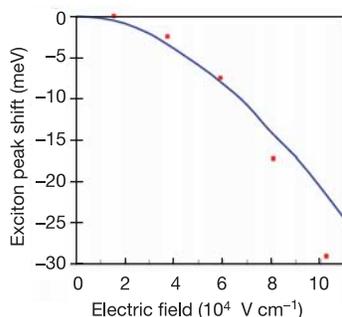


Figure 4 | Shifts of exciton peaks. Comparison of heavy-hole exciton peak shift from measurements (filled squares) and tunnelling resonance calculations (sum of electron and hole level shifts) (line).

despite the possibility of scattering to the lower indirect valleys.

With a reverse bias from 0 to 4 V, both peaks are red shifted by the QCSE. The e-hh exciton is shifted from 1,408 nm (0 V) to 1,456 nm (4 V). The maximum effective absorption coefficient change is $2,800 \text{ cm}^{-1}$ at 1,438 nm under 3 V bias. This is, to our knowledge, the first efficient electro-absorption modulation observed in group-IV materials, and its performance is comparable to high-quality (direct gap) III-V materials at similar wavelengths (see, for example, ref. 19). The clarity of the exciton peaks in the presence of a field is actually better than that of typical III-V structures at such wavelengths¹⁹, and the electroabsorption shows much clearer shifts than previous electroabsorption measurements in indirect III-V materials²⁵. With a 4 V bias, the absorption coefficient contrast is greater than 3 over a bandwidth ranging from 1,443 to 1,471 nm, with a peak value of 4.69 at 1,461 nm.

The possibility of operating different quantum-well designs at say, 1,550 nm, which is compatible with long-distance telecommunications, will be the subject of future work. We also anticipate that waveguide modulator structures will be realizable using appropriate materials for waveguide cladding layers.

The measured shift agrees with simulated results (Fig. 4) obtained via the tunnelling resonance method^{5,18}. In addition, we evaluated the exciton binding energy shift as in ref. 18, using numerically evaluated electron and hole wavefunctions, though this correction is $<1 \text{ meV}$ and is neglected here. We used a Γ -valley electron effective mass of $0.041m_0 + 0.115(1-x)m_0$, and a heavy-hole effective mass of $0.28m_0 + 0.21(1-x)m_0$, where m_0 is the free electron mass and x is the Ge concentration^{26,27} (the relevant silicon (001) hole mass is based on Luttinger parameters²⁸).

We have demonstrated efficient QCSE in silicon-based structures, using strained Ge MQWs. The behaviour of the exciton peaks, the band edge shift and the shift in absorption coefficient are comparable to those observed in III-V materials at similar wavelengths. Our materials and fabrication processes are completely CMOS compatible and suitable for mass production. This approach is therefore very promising for silicon-based electro-absorption modulators operating at high speed, low power, low operating voltage and with small device areas.

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